

REMARKS

Applicants will address each of the Examiner's objections and rejections in the order in which they appear in the Final Rejection. As an RCE is being filed herewith, it is requested that this amendment be entered and considered.

II. Claim Rejections – 35 USC §103

The Examiner has a number of rejections under 35 USC §103. Applicants will address each in the order in which they appear in the Final Rejection

A. Rejection Over Yamazaki et al.’ 887

In the Final Rejection, the Examiner continues to reject Claims 1-4, 6-9, 14-17 and 19-22 under 35 USC §103 as being unpatentable over Yamazaki et al. ‘887.

While this rejection is respectfully traversed, in order to advance the prosecution of this application, Applicants are amending Claims 1, 4, 6, 9, 14, 17, 19 and 22 to recite that the gate electrode comprises a first conductive layer in contact with a gate insulating film, a second conductive layer in contact with the first conductive layer, and a third conductive layer in contact with the gate insulating film, side surfaces of the first conductive layer, and top and side surfaces of the second conductive layer, and that the second conductive layer comprises a different material from the first conductive layer.

In contrast, Fig. 1 of Yamazaki ‘887 shows a gate electrode (107 or 113) that appears to comprise one layer which is made of one type of material. This is different than the gate electrode of the claimed invention which comprises three layers, in which the first and second conductive layers are made of different materials.

Hence, the claimed invention is not disclosed or suggested by Yamazaki '887. Accordingly, it is respectfully requested that this rejection be withdrawn.

B. Rejection Over Miyasaka et al.

The Examiner also continues to reject Claims 11 and 12 under 35 USC §103 as being unpatentable over Miyasaka et al.

While this rejection is also respectfully traversed, in order to advance the prosecution of this application, Applicants are amending Claims 11 and 12 to recite that an n-channel TFT comprises first and second gate electrodes in which the second gate electrode partially overlaps LDD regions while the first gate electrode does not overlap the LDD regions, and that a p-channel TFT comprises third and fourth gate electrodes in which the fourth gate electrode partially overlaps source and drain regions while the third gate electrode does not overlap the source and drain regions. This amendment clarifies the feature of the claimed invention that one of two gate electrodes overlaps the LDD regions or the source and drain regions.

Applicants respectfully submit that this is not disclosed or suggested by Miyasaka. Accordingly, it is respectfully requested that this rejection be withdrawn.

C. Rejection Over Miyaska et al. in view of Johnson

The Examiner also rejects Claims 24-25 under 35 USC §103 as being unpatentable over Miyasaka et al. in view of Johnson.

While this rejection is also respectfully traversed, in order to advance the prosecution of this application, Applicants are amending Claims 24 and 25 in a similar manner to that of Claims 11-12.

Therefore, for similar reasons to that discussed above, these claims are also not disclosed or suggested by the cited references. Hence, it is respectfully requested that this rejection be withdrawn

Accordingly, for at least the above-stated reasons, it is respectfully submitted that the claims are patentable, and the rejections under §103 should be withdrawn.

Information Disclosure Statement

Applicants are submitting herewith an Information Disclosure Statement. It is respectfully requested that this IDS be considered prior to the issuance of any further action on this application.

Conclusion


It is respectfully submitted that the present application is now in a condition for allowance, and it is requested that it now be allowed.

If any fee is due for this amendment, please charge our deposition account 50/1039.

Favorable reconsideration is earnestly solicited.

Respectfully submitted,

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